**Bq2419x Driver Command Reference Sheet**

Input Source Control Register REG00

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| --- | --- | --- | --- |
| Bit | Option | Command | Valid Inputs |
| 7 | EN\_HIZ | bqEnHIZ() | ENABLE  DISABLE |
| 3:6 | Input Voltage Limit  VINDPM | bqSetVINDPM()  *Input must be an integer in the proper range. Code will be set to the closest setting that is not above the requested value. (mV)* | 3880 – 5080 |
| 0:2 | Input Current Limit  INLIM | bqSetIINDPM() | IINLIM\_100MA  IINLIM\_150MA  IINLIM\_500MA  IINLIM\_900MA  IINLIM\_1200MA  IINLIM\_1500MA  IINLIM\_2000MA  IINLIM\_3000MA |

Power-On Configuration Register REG01

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| --- | --- | --- | --- |
| Bit | Option | Command | Valid Inputs |
| 7 | Register Reset | bqRstREG() | -- |
| 6 | I2C Watchdog Timer Reset | bqRstWatchDog() | -- |
| 4:5 | Charger Configuration  CHG\_CONFIG | bqSetCHGCONFIG() | DISABLE  CHARGE\_BATTERY  OTG |
| 1:3 | Minimum System Voltage Limit  SYS\_MIN | bqSetSYSMIN()  *Input must be an integer in the proper range. Code will be set to the closest setting that is not above the requested value. (mV)* | 3000 – 3700 |
| 0 | USB\_OTG | bqSetOTGILIM() | BOOSTLIM\_500mA  BOOSTLIM\_1300mA |

Charge Current Control Register REG02

|  |  |  |  |
| --- | --- | --- | --- |
| Bit | Option | Command | Valid Inputs |
| 2:7 | Fast Charge Current Limit  ICHG | bqSetFASTCHRG()  *Input must be an integer in the proper range. Code will be set to the closest setting that is not above the requested value. (mA)* | 500 – 4532 |
| 0:1 | NOT IN USE | -- | -- |

Pre-Charge/Termination Current Control Register REG03

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| --- | --- | --- | --- |
| Bit | Option | Command | Valid Inputs |
| 4:7 | Pre-Charge Current Limit  IPRECHG | bqSetPRECHRG()  *Input must be an integer in the proper range. Code will be set to the closest setting that is not above the requested value. (mA)* | 128 – 2048 |
| 0:3 | Termination Current Limit  ITERM | bqSetTERMCHRG()  *Input must be an integer in the proper range. Code will be set to the closest setting that is not above the requested value. (mA)* | 128 – 2048 |

Charge Voltage Control Register REG04

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| --- | --- | --- | --- |
| Bit | Option | Command | Valid Inputs |
| 2:7 | Charge Voltage Limit  VREG | bqSetChgVoltage()  *Input must be an integer in the proper range. Code will be set to the closest setting that is not above the requested value. (mV)* | 3504 – 4512 |
| 1 | BATLOWV | bqSetBATLOWV() | BATLOWV\_2800mV  BATLOWV\_3000mV |
| 0 | Battery Recharge Threshold  VRECHG | bqSetRECHRG() | VRECHG\_100mV  VRECHG\_300mV |

Charge Termination/Timer Control Register REG05

|  |  |  |  |
| --- | --- | --- | --- |
| Bit | Option | Command | Valid Inputs |
| 7 | Termination Setting  EN\_TERM | bqEnTERM() | ENABLE  DISABLE |
| 6 | Termination Indicator Threshold  TERM\_STAT | bqTERMSTAT() | TERMSTAT\_ITERM  TERMSTAT\_EARLY |
| 4:5 | I2C Watchdog Timer Limit  WATCHDOG | bqSetWatchDog() | DISABLE  WatchDog\_40s  WatchDog\_80s  WatchDog\_160s |
| 3 | Safety Timer Setting  EN\_TIMER | bqEnTIMER() | ENABLE  DISABLE |
| 1:2 | Fast Charge Timer  CHG\_TIMER | bqSetFastChgTimer() | CHGTIMER\_5h  CHGTIMER\_8h  CHGTIMER\_12h  CHGTIMER\_20h |
| 0 | NOT IN USE | -- | -- |

IR Compensation / Thermal Regulation Control Register REG06

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| --- | --- | --- | --- |
| Bit | Option | Command | Valid Inputs |
| 5:7 | IR Compensation Resistor Setting  BAT\_COMP | bqSetBATCOMP()  *Input must be an integer in the proper range. Code will be set to the closest setting that is not above the requested value. (mΩ)* | 0 – 70 |
| 2:4 | IR Compensation Voltage Clamp  VCLAMP | bqSetVCLAMP()  *Input must be an integer in the proper range. Code will be set to the closest setting that is not above the requested value. (mV)* | 0 – 112 |
| 0:1 | Thermal Regulation Threshold  TREG | bqSetTREG | TREG\_60C  TREG\_80C  TREG\_100C  TREG\_120C |

Misc. Operation Control Register REG07

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| --- | --- | --- | --- |
| Bit | Option | Command | Valid Inputs |
| 7 | DPDM\_EN | bqEnDPDM() | ENABLE  DISABLE |
| 6 | TMR2X\_EN | bqEnTMR2X() | ENABLE  DISABLE |
| 5 | BATFET\_Disable | bqOffBATFET() | ENABLE  DISABLE |